

Substitute for form 1449A/B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	10/812,062
				Filing Date	March 30, 2004
				First Named Inventor	Barin Geoffry HASKELL
				Art Unit	2631
				Examiner Name	Anner N. Holder
Sheet	1	of	2	Attorney Docket Number	13316/3293

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	1	ZHIHAI HE, Y.K. KIM, and S.K. MITRA, "Low-delay rate control for DCT video coding via $\rho$ -domain source modeling," IEEE Trans. on Circuits and Systems for Video Technology, Aug. 2001, vol. 11, no. 8	
	2	ZHIHAI HE and S.K. MITRA, "Optimum bit allocation and accurate rate control for video coding via $\rho$ -domain source modeling," IEEE Trans. on Circuits and Systems for Video Technology, Oct. 2002, pp. 840-849, vol. 12, no. 10	
	3	ZHIHAI HE and S.K. MITRA, "A unified rate-distortion analysis framework for transform coding," IEEE Trans. on Circuits and Systems for Video Technology, Dec. 2001, pp. 1221-1236, vol. 11, no. 12	
	4	WEI DING, "Joint encoder and channel rate control of VBR video over ATM networks," IEEE Trans. on Circuits and Systems for Video Technology, Apr. 1996, pp. 266-278, vol. 7, no. 2	
	5	WEI DING and B. LIU, "Rate control of MPEG video coding and recoding by Rate-Quantization modeling," IEEE Trans. on Circuits and Systems for Video Technology, Feb. 1996, pp. 12-20, vol. 6, no. 1	
	6	I-MING PAO and MING-TING SUN, "Encoding stored video for streaming applications," IEEE Trans. on Circuits and Systems for Video Technology, Feb. 2001, pp. 199-209, vol. 11, no. 2	
	7	JORDI RIBAS-CORBERA and S.-M. LEI, "A frame-layer bit allocation for H.263+," IEEE Trans. on Circuits and Systems for Video Technology, Oct. 2000, pp. 1154-1158, vol. 10, no. 7	
	8	YAN YANG and S.S. HEMAMI, "Rate control for VBR video over ATM: Simplification and implementation," IEEE Trans. on Circuits and Systems for Video Technology, Nov. 2001, pp. 1045-1058, vol. 11, no. 9	
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	11	LILLA BOROCZKY, A.Y. NGAI, and E.F. WESTERMAN, "Joint rate-control with look-ahead for multi-program video coding," IEEE Trans. on Circuits and Systems for Video Technology, Oct. 2000, pp. 1159-1163, vol. 10, no. 7	

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	12	JORDIN RIBAS-CORBERA and S. LEI, "Rate control in DCT video coding for low-delay communications," IEEE Trans. on Circuits and Systems for Video Technology, Feb. 1999, pp. 172-185, vol. 9, no. 1	
	13	PO-YUEN CHENG, J. LI, and C.-C.J. Kuo, "Rate control for and embedded wavelet video coder," IEEE Trans. on Circuits and Systems for Video Technology, Aug. 1997, pp. 696-702, vol. 7, no. 4	
	14	KUO-CHIN FAN and K.-S. KAN, "An active scene analysis-based approach for pseudoconstant bit-rate video coding," IEEE Trans. on Circuits and Systems for Video Technology, Apr. 1998, pp. 159-170, vol. 8, no. 2	
	15	ASHISH JAGMOHAN and K. RATAKONDA, "MPEG-4 one-pass VBR rate control for digital storage," IEEE Trans. on Circuits and Systems for Video Technology, May 2003, pp. 447-452, vol. 13, no. 5	
	16	ANTHONY VETRO, H. SUN, and Y. WANG, "MPEG-4 rate control for multiple object coding," IEEE Trans. on Circuits and Systems for Video Technology, Feb. 1999, pp. 186-199, vol. 9, no. 1	
	17	JOSE I. RONDA, F. JAUREGUIZAR, and N. GARCIA, "Rate control and bit allocation for MPEG-4," IEEE Trans. on Circuits and Systems for Video Technology, Dec. 1999, pp. 1243-1258, vol. 9, no. 8	
	18	HUNG-JU LEE, T. CHIANG, and Y.-Q. ZHANG, "Scalable rate control for MPEG-4 video," IEEE Trans. on Circuits and Systems for Video Technology, Sept. 2000, pp. 878-894, vol. 10, no. 6	
	19	FENG PAN, Z. LI, K. LIM, and G. FENG, "A study of MPEG-4 rate control scheme and its improvements," IEEE Trans. on Circuits and Systems for Video Technology, May 2003, pp. 440-446, vol. 13, no. 5	
	20	JEONG-WOO LEE, A. VETRO, Y. WANG, and Y.-S. HO, "Bit allocation for MPEG-4 video coding with spatio-temporal tradeoffs," IEEE Trans. on Circuits and Systems for Video Technology, June 2003, pp. 488-502, vol. 13, no. 6	

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